



IEEE Xplore®

RELEASE 1.5

[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)
[Quick Links](#)
» [See](#)

Welcome to IEEE Xplore®

- ☐ [Home](#)
- ☐ [What Can I Access?](#)
- ☐ [Log-out](#)

Tables of Contents

- ☐ [Journals & Magazines](#)
- ☐ [Conference Proceedings](#)
- ☐ [Standards](#)

Search

- ☐ [By Author](#)
- ☐ [Basic](#)
- ☐ [Advanced](#)

Member Services

- ☐ [Join IEEE](#)
- ☐ [Establish IEEE Web Account](#)
- ☐ [Access the IEEE Member Digital Library](#)

[Print Format](#)

Your search matched **3** of **990765** documents.

A maximum of **3** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.

You may refine your search by editing the current search expression or entering a new one the text box.

Then click **Search Again**.

((programmable)and (access)) and(device) and ext

Results:

Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD**

1 A high performance microprocessor for DSP applications

Roesgen, J.;

Acoustics, Speech, and Signal Processing, IEEE International Conference on IC/ '86. , Volume: 11 , Apr 1986

Page(s): 397 -400

[\[Abstract\]](#) [\[PDF Full-Text \(59 KB\)\]](#) **IEEE CNF**

2 The Motorola DSP 96002 IEEE floating-point digital signal processor

Kloker, K.L.; Lindsley, B.; Liberman, S.; Marino, P.; Rushinek, E.; Hillman, G.D
Acoustics, Speech, and Signal Processing, 1989. ICASSP-89., 1989 International Conference on , 23-26 May 1989

Page(s): 2480 -2483 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) **IEEE CNF**

3 The DSP32C: AT&Ts second generation floating point digital signal processor

Fuccio, M.L.; Gadenz, R.N.; Garen, C.J.; Huser, J.M.; Ng, B.; Pekarich, S.P.; Ull K.D.;

Micro, IEEE , Volume: 8 Issue: 6 , Dec. 1988

Page(s): 30 -48

[\[Abstract\]](#) [\[PDF Full-Text \(1864 KB\)\]](#) **IEEE JNL**